

PRIOR ART

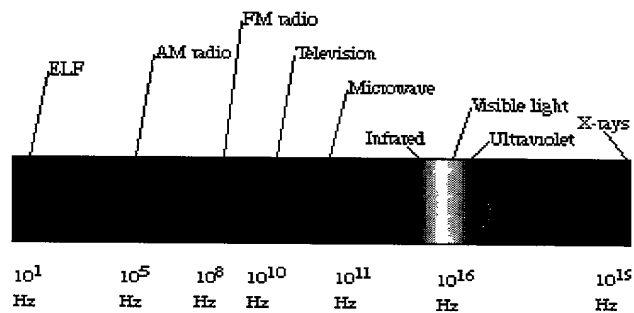


Fig. 1A

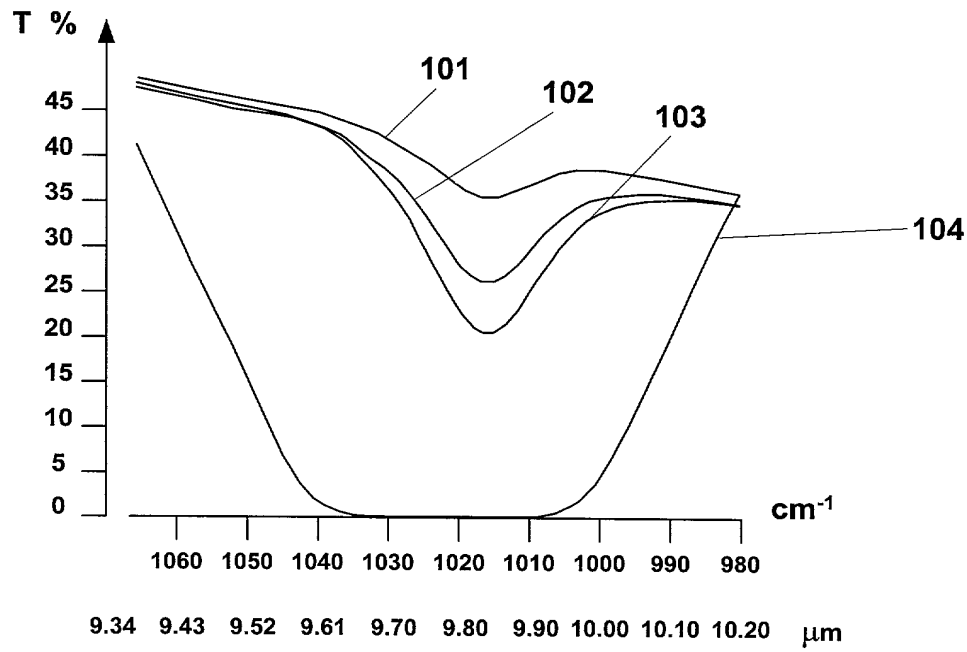


Fig. 1B

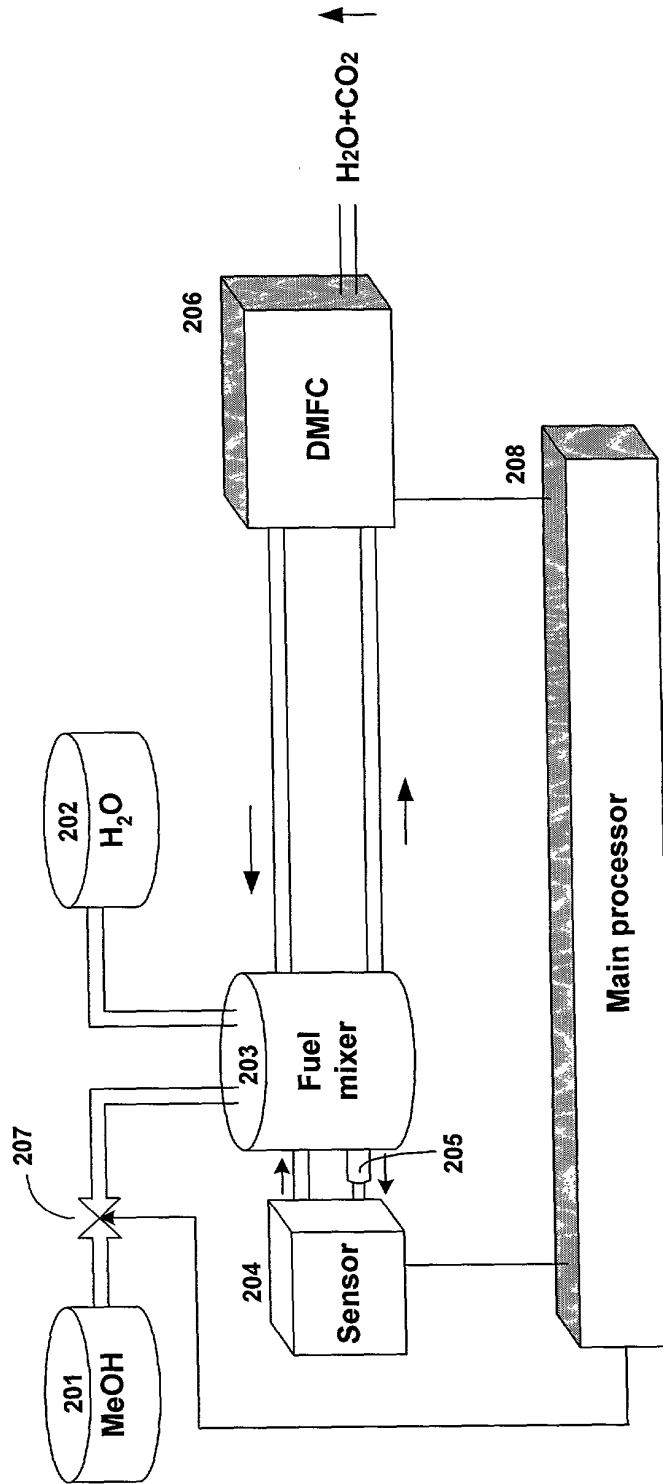
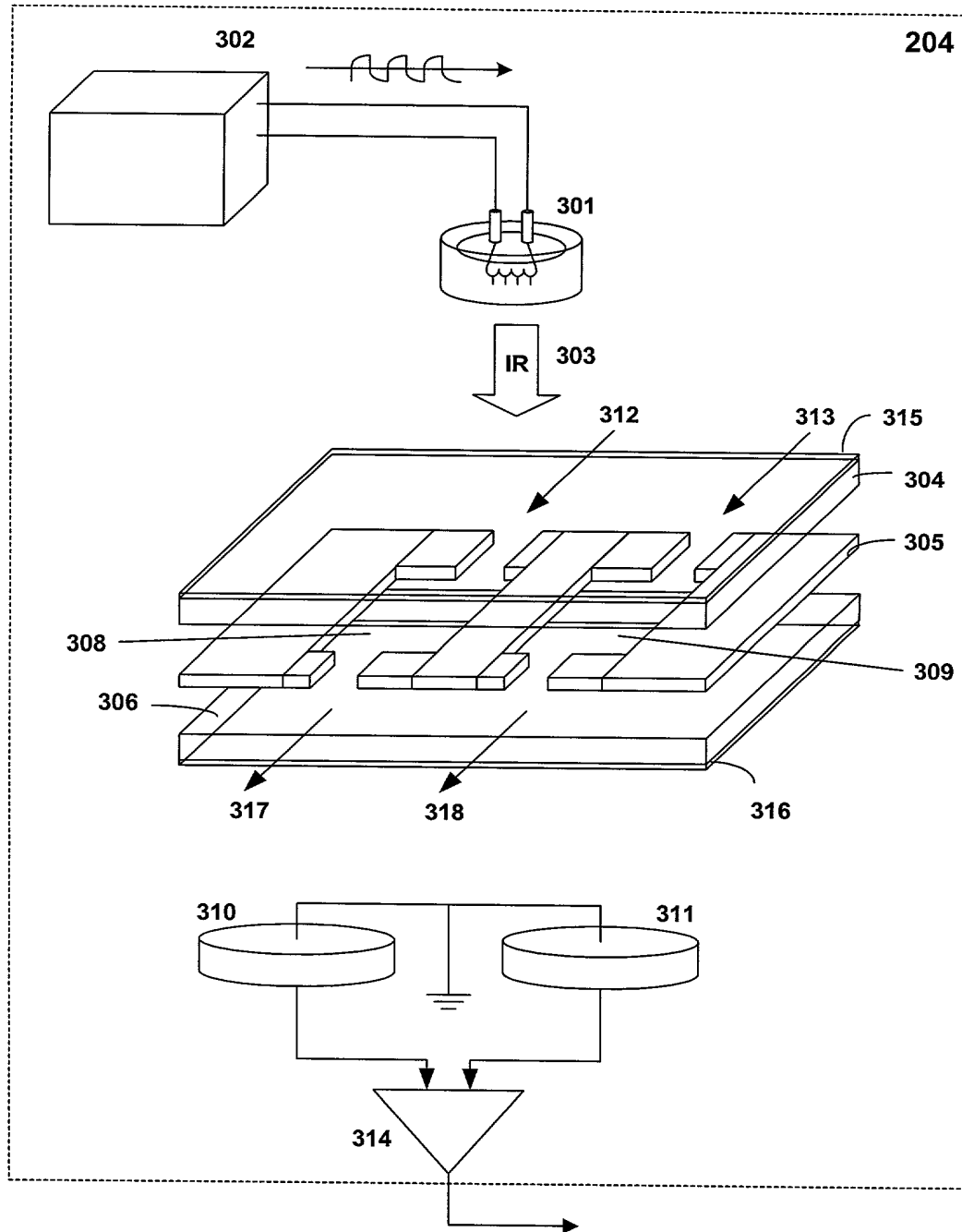


Fig. 2

FIG. 2 is a schematic diagram of a DMFC system.

Fig. 3



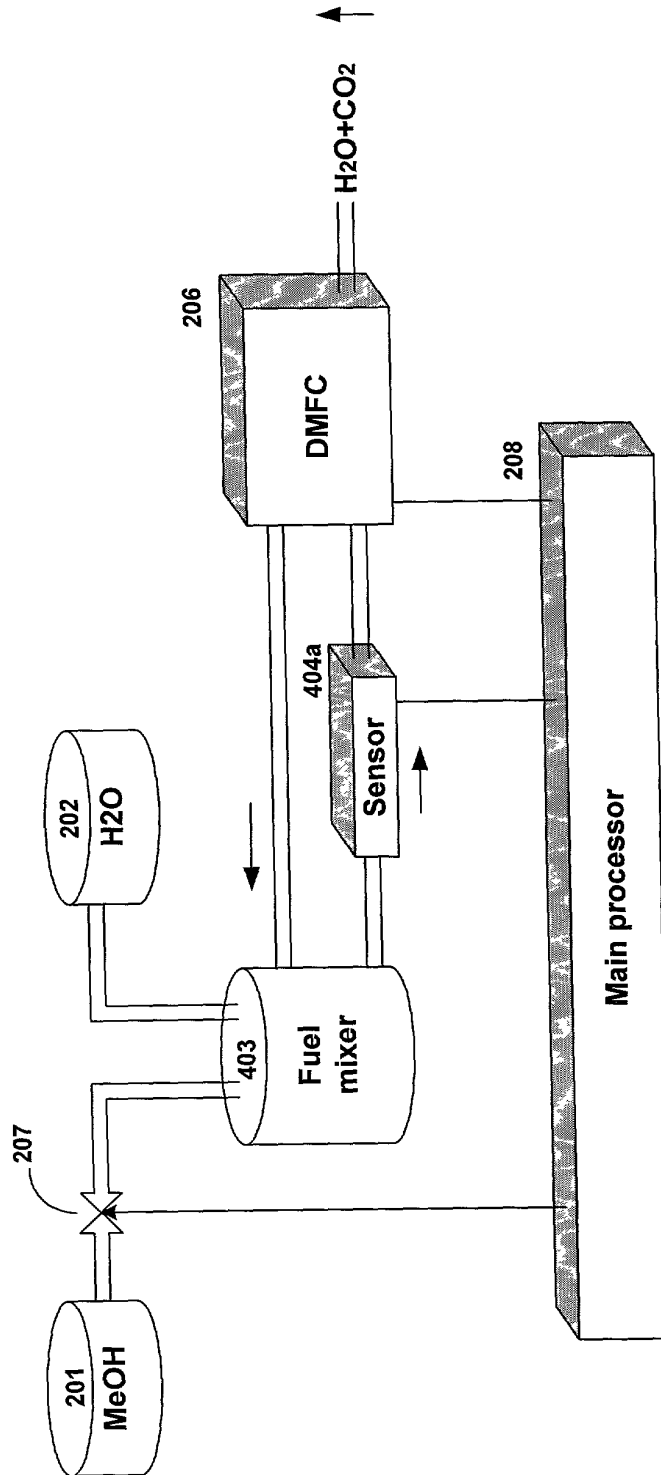


Fig. 4A

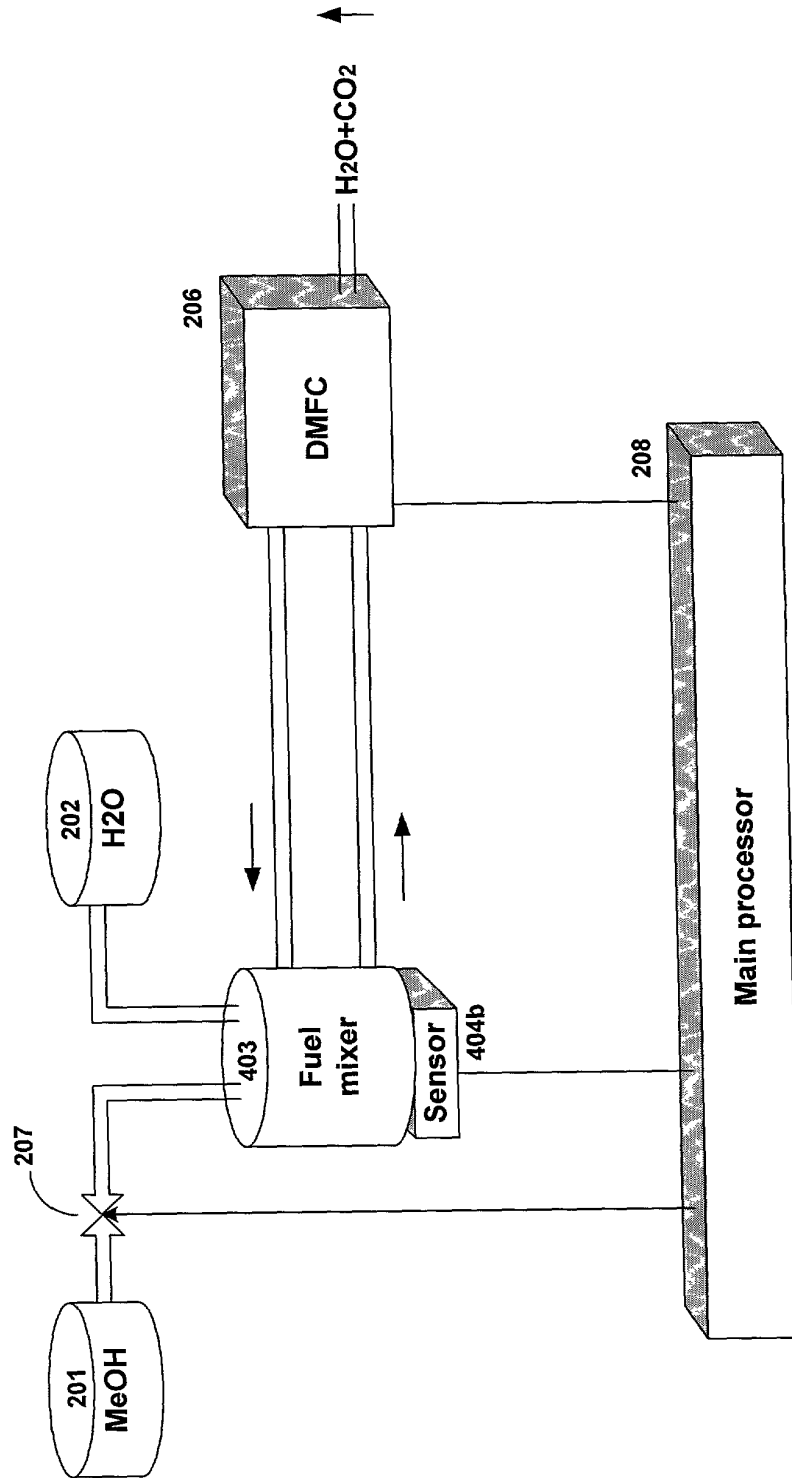


Fig. 4B

FIG. 4B

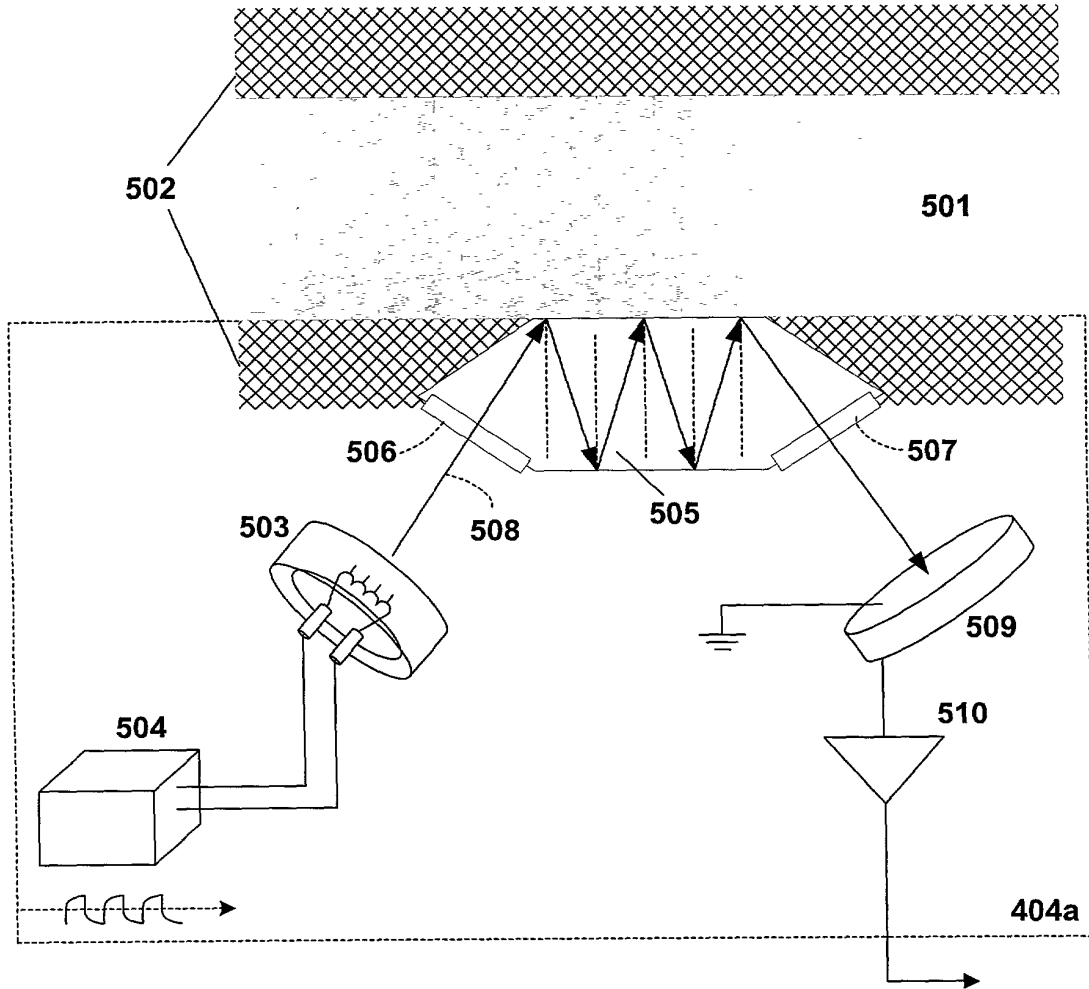


Fig. 5A

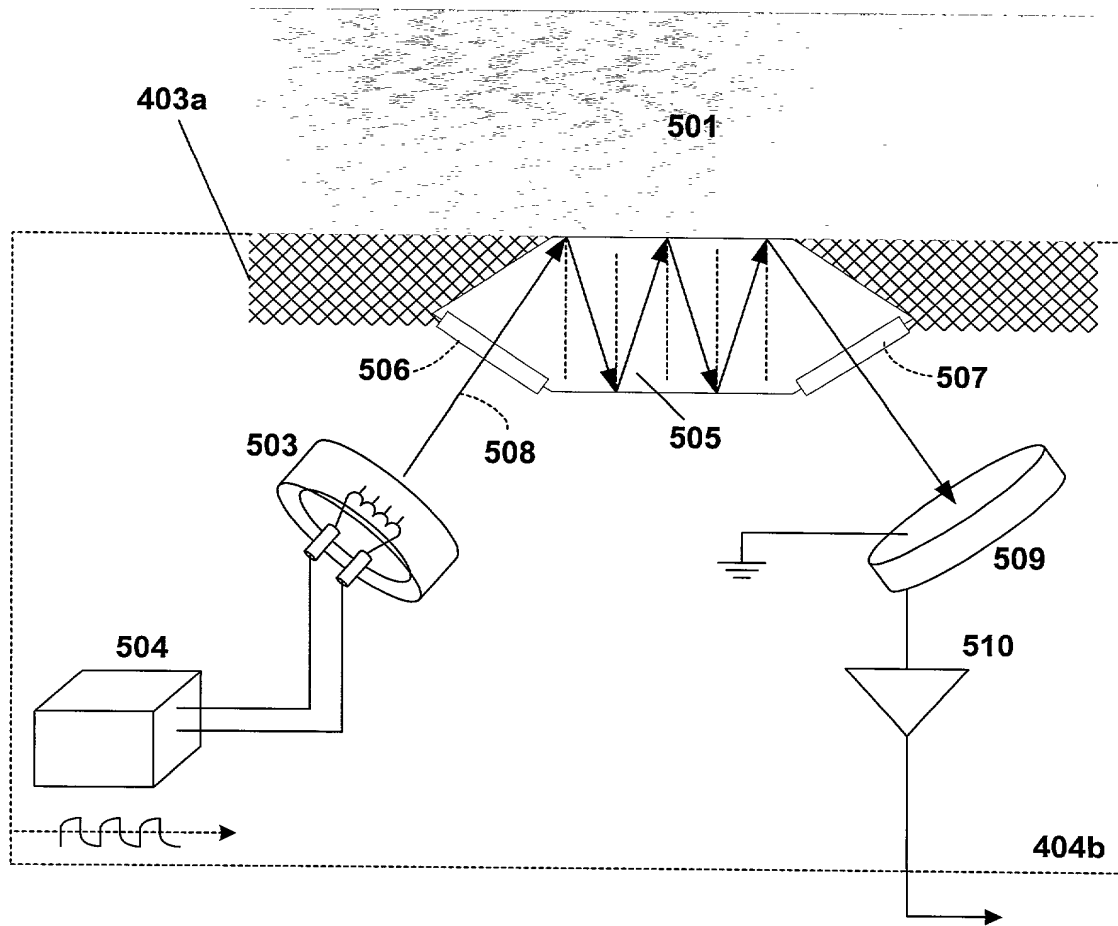


Fig. 5B